

NOTICE

The First Real Time Admission for filling in the vacant seats in the **M.Tech VLSI & Embedded System, Programme** is scheduled to be held on **17th July Friday** at the Department of Electronics from among the candidates included in the **DAT** rank list.

Registration Time: 10.00 a.m.

Candidates who arrive after **11 a.m.** are not permitted to attend the Real-Time admission.

Vacancy Positions:

SL No	Reservation	Vacancy
1	LCC	1
2	KMB	1
3	SSD	1
4	LKD	1
5	TSG	2
6	IDC	1
7	DAC	1
8	CUT	2
9	OBH	1

Fee structure to be paid online

LKD/TSG/SSD/IDC/DAC/CUT/OBH LCC -34242/-	KMB -Rs.3000/-
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List of all Documents (with two self-attested copies) to be produced at the time of Counselling.

1. Certificate and mark list of all educational qualifications (from 10th class onwards)
2. Certificate to prove eligible reservation
3. Transfer certificate and valid Conduct certificate
4. Birth certificate/Nativity Certificate

DEPARTMENT OF ELECTRONICS

COCHIN UNIVERSITY OF SCIENCE AND TECHNOLOGY

COCHIN – 682 022

2026 ADMISSION

Sl. No.	FEE CATEGORY NAME	LKD/TSG/SSD/IDC/DAC/CUT/OBH LCC (₹)	KMB(₹)
1	Arts Fee	600	
2	Campus Radio Fee	600	
3	Caution Deposit	3000	3000
4	Co-curricular fee	300	
5	Laboratory Fee per semester category 1-2026	1000	
6	Library Fee	500	
7	Tuition Fee	19600	
8	Medical Inspection Fee	200	
9	Quasi University Fee	400	
10	RFID Card	400	
11	Sports Fee	700	
12	Stationery Fee	700	
13	Students Aid	500	
14	University Union Fee	600	
15	Group Insurance Scheme	142	
16	One time establishment fee for two years Programmes 2026	5000	
15			
	Total:	34242/-	3000/-

OTHER FEE APPLICABLE TO THE COURSE

1. PTA@ ₹ 3000
2. DDF@₹2000
3. ALUMINI@₹800

NB: To be Paid on or Before 31st July 2026